

24-3-17

Max marks:30

Mid Semester Examination



SCHOOL OF TECHNOLOGY

Integrated MCA Semester IV

STM 528:-Microprocessors And Applications

Section A

1 x 6

1. Define Instruction Queue.
2. What is an assembler directive? Give any 2 examples.
3. Two methods of memory interfacing are _____ and _____.
4. Define clock cycle?
5. Name any 4 built-in functions in emu8086.inc
6. Write about TEST instruction.

Section B

3 x 4

Attempt any four questions

1. Give a brief description of generic memory interfacing with block diagram.
2. Differentiate between shift and rotate instructions with an example for each.
3. Discuss about the register set of 8086 in detail.
4. Write about Logical instructions.
5. Briefly explain about the timing diagrams with the help of any timing diagram (read/write cycle).

Section C

6 x 2

Attempt any two questions

1. Explain about the architecture of 8086 with diagram.
2. Explain about 8255-PIO/PPI with its pin diagram.
3. Discuss about various addressing modes of 8086 in detail with examples.