Max marks:30

End Semester Examination

SCHOOL OF PHYSICAL SCIENCE

DOON UNIVERSITY,DEHRADUN

Integrated Msc Computer Science III semester

CSC 202:Microprocessor And Assembly Programming

Section A   1 x 6

1. Define Machine Cycle .
2. What is an Instruction Queue?
3. Which are the different categories of interrupts in 8086?
4. What is an Assembler Directive?Give two examples.
5. Define I/O Interfacing.
6. Intel 8259 A is a/an _____ for 8086 microprocessor

Section B   3 x 4

Attempt any four questions

1. Differentiate between Shift and Rotate instructions with illustration using an example.
2. Discuss about DMA mechanism.
3. Explain the gneric memory interfacing with block diagram .
4. Write about the register set of 8086 .
5. Describe the Instruction format in detail.

Section C   6 x 2

Attempt any two questions

1. Explain about 8237 DMA controller with the help of its pin diagram.
2. Discuss about the architecture of 8086 with block diagram.
3. Discuss about various addressing modes in detail with examples for each.